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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Kyu-Nam Lim			
					Filing Date		Group 2816	
					October 5, 2001		Unknown	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
WHR	AA	3,991,380	09 Nov. 1976	Pryor	330	13		
WHR	AB	4,206,418	03 June 1980	Dingwall	330	253		
WHR	AC	5,602,495	11 Feb. 1997	Lou	326	71		
WHR	AD	6,023,174	8 Feb. 2000	Kirsch	326	34		
WHR	AE	6,169,424 B1	2 Jan. 2001	Kurd	327	53		
WHR	AF	6,172,524 B1	9 Jan. 2001	Cho	326	70		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
WHR	AG	GBG2343574 A	10/05/00	United Kingdom	/	/		
WHR	AH	EP0146910 A2	03/07/85	Europe	/	/		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
WHR	AI	M. Bazes, "Two Novel Fully Complementary Self-Biased CMOS Differential Amplifiers", IEEE Journal of Solid-State Circuits, Vol. 26, pp. 165-168, February 1991.						
WHR	AI	Y. Takai, M. Fujita, et al. "A 250 Mbps/pin, 1Gb Double Data Rate SDRAM with a Bidirectional Delay and an Inter-bank shared Redundancy Scheme", ISSCC 99 Digest of Technical Papers, 418-419 & 487, February 1999.						
Examiner		Date Considered 1/8/2002						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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